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				Filing Date	February 28, 2002
				First Named Inventor	Stephen M. Trimberger
				Art Unit	Unknown
				Examiner Name	Unknown
Sheet	2	of	2	Attorney Docket Number	X-874 US

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OTHER -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		W. Bruce Culbertson, Rick Amerson, Richard J. Carter, Philip Kuekes, Greg Snider; "Defect Tolerance on the Teramac Custom Computer"; The 5th Annual IEEE Symposium on Field-Programmable Custom Computing Machines; April 16-17, 1997; pp. 116-123.	
		Fran Hanchek; Shantanu Dutt; "Node-Covering Based Defect and Fault Tolerance Methods for Increased Yield in FPGAs"; The Proceedings of the Ninth International Conference on VLSI Design; January 1996.	
		Xilinx, Inc.; Virtex-II Platform FPGA Handbook; published December 6, 2000; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; pp. 33-75.	

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